

FIG. 1

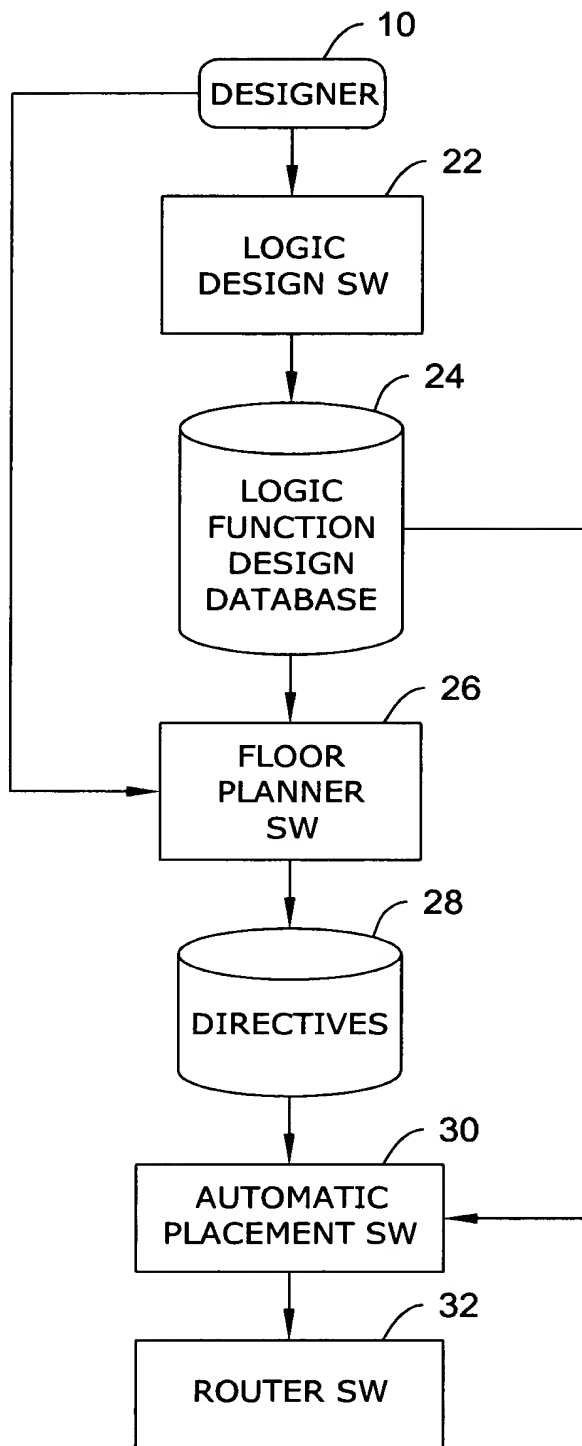
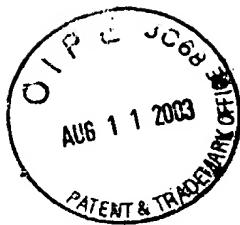


FIG. 2

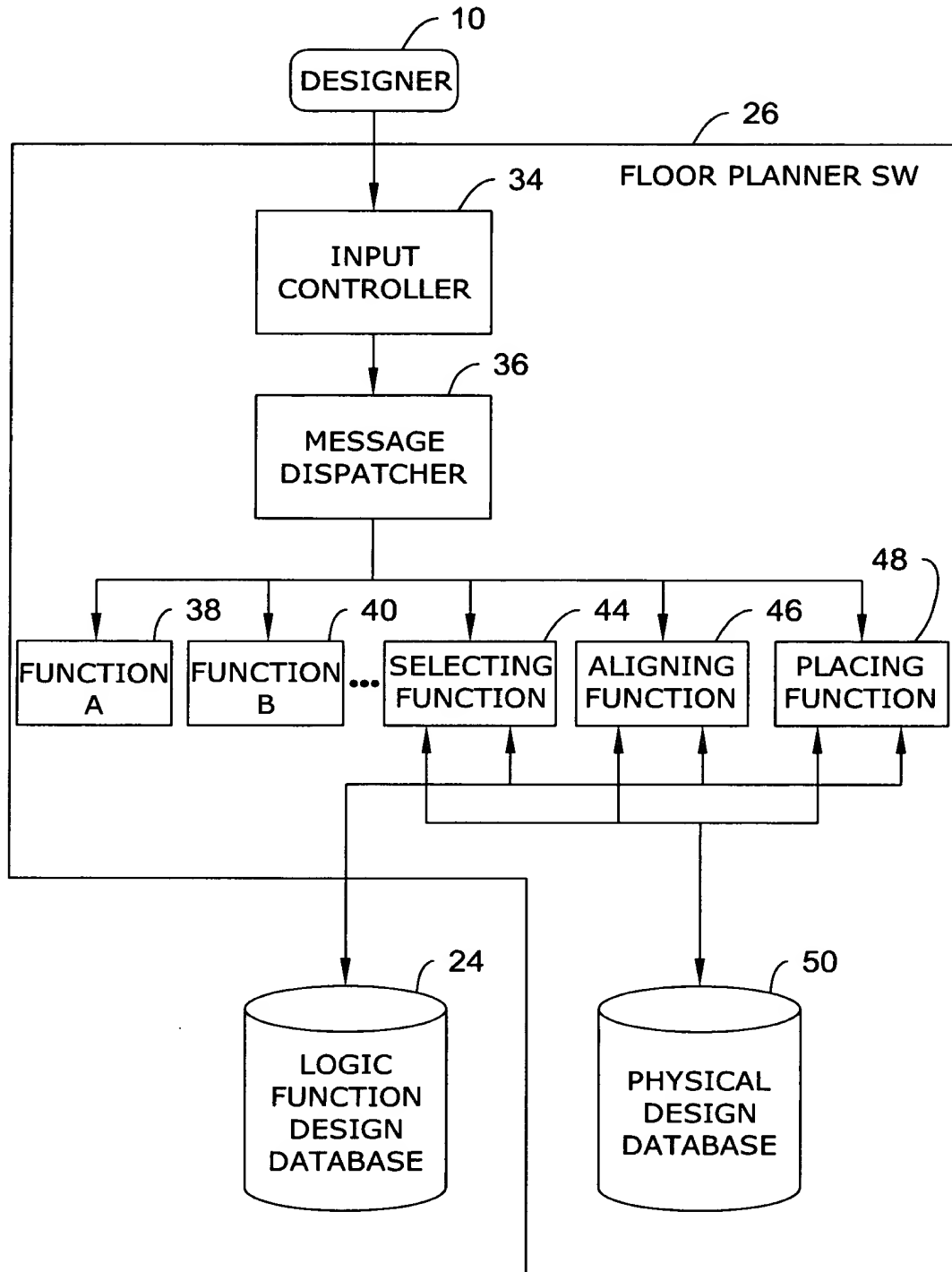
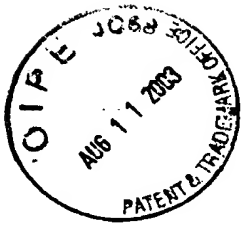


FIG.3

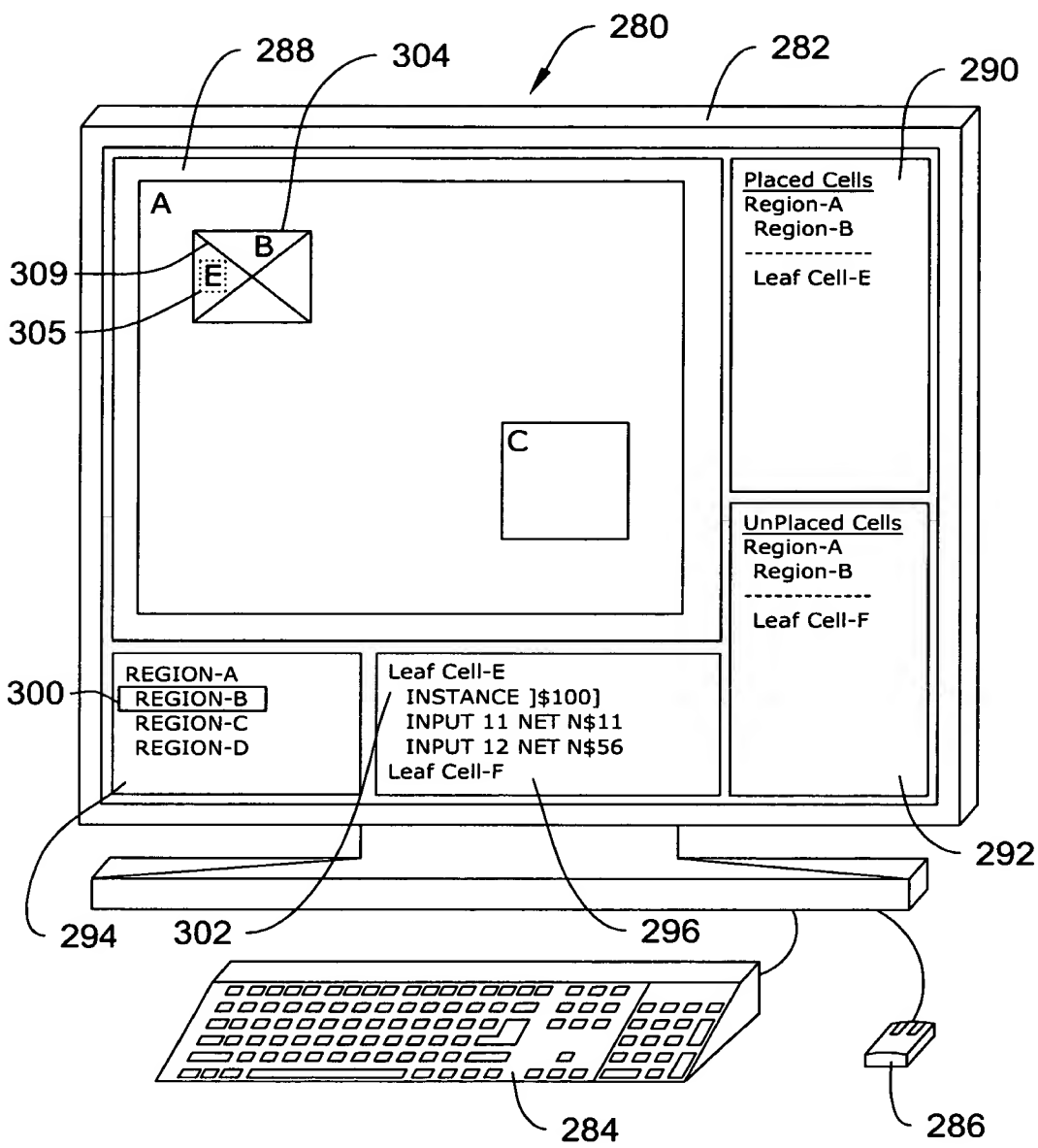


FIG. 4

FIG. 5

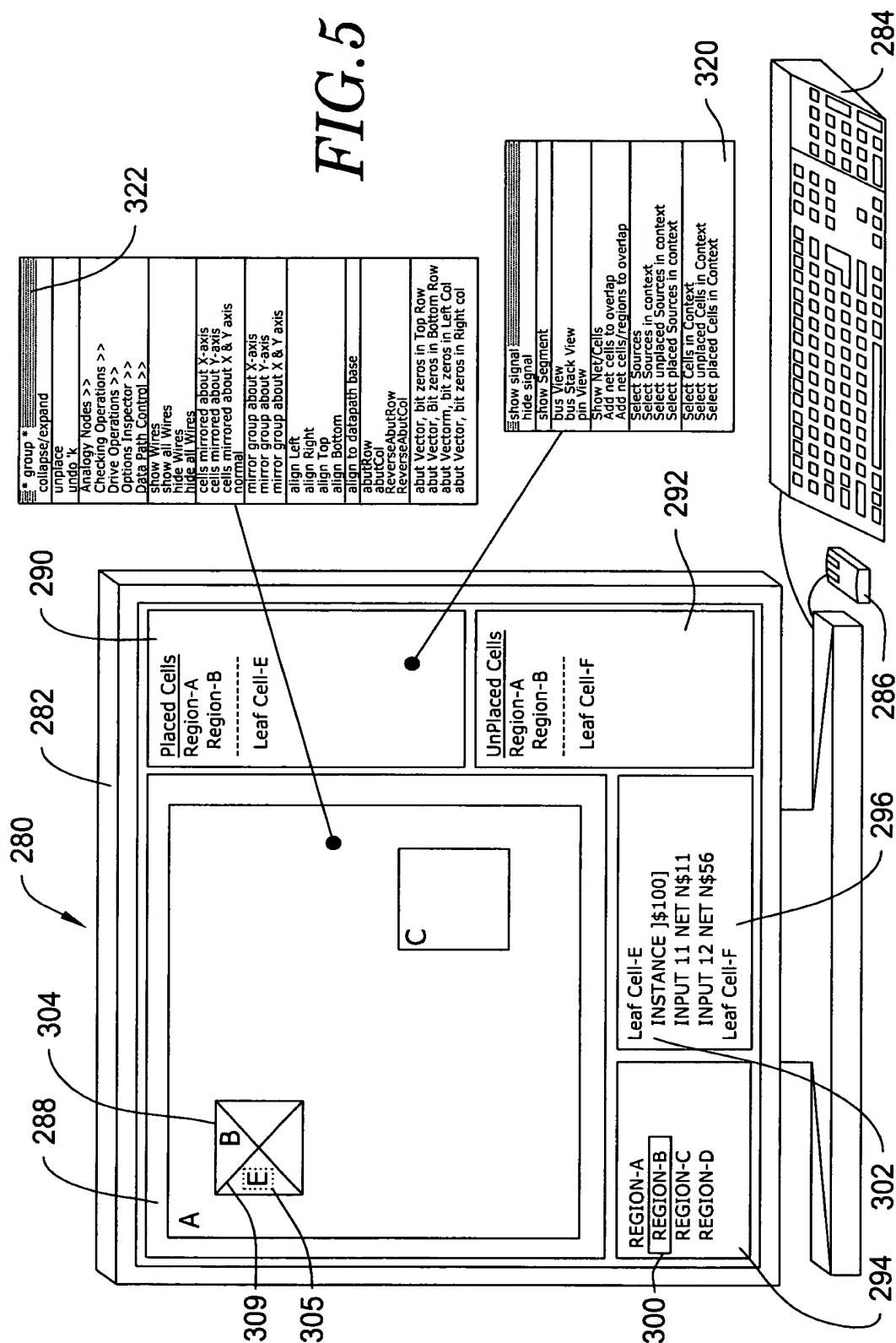




FIG. 6



UNPLACED

SELECT SOURCES FOR NET A(0-7)

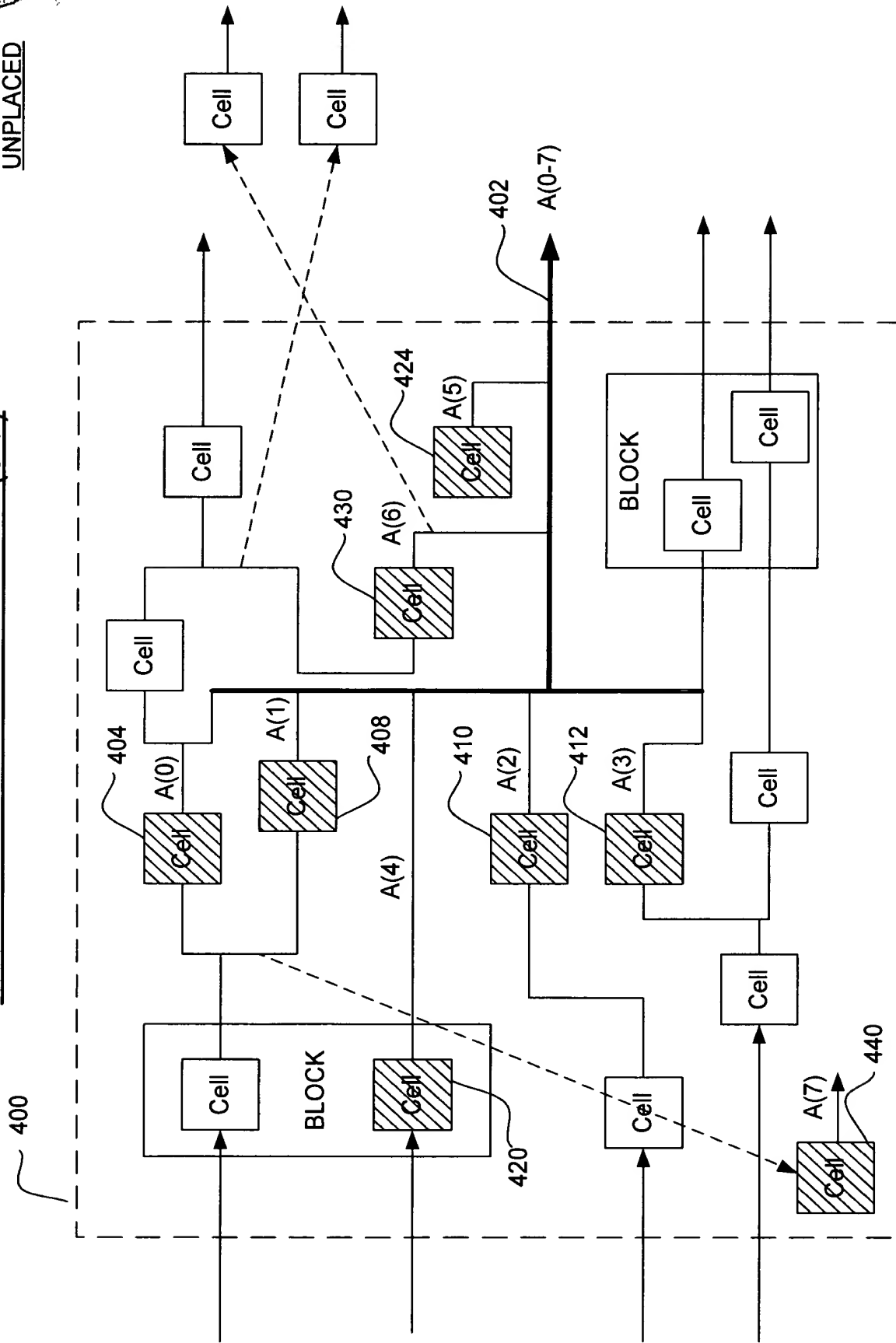


FIG. 7



SELECT SOURCES IN CONTEXT FOR NET A(0-7)

UNPLACED

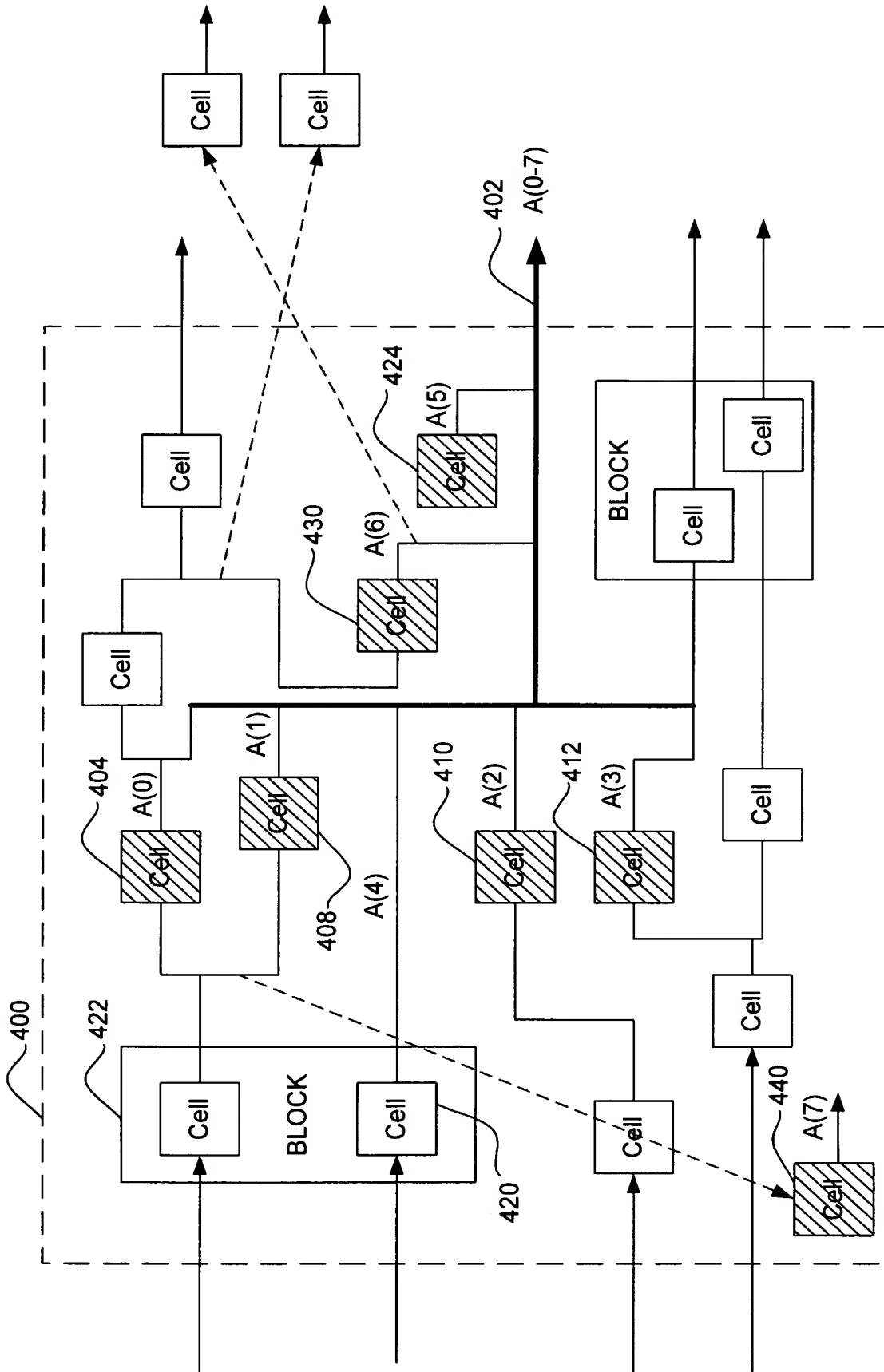


FIG. 8



SELECT UNPLACED SOURCES IN CONTEXT FOR NET A(0-7)

UNPLACED

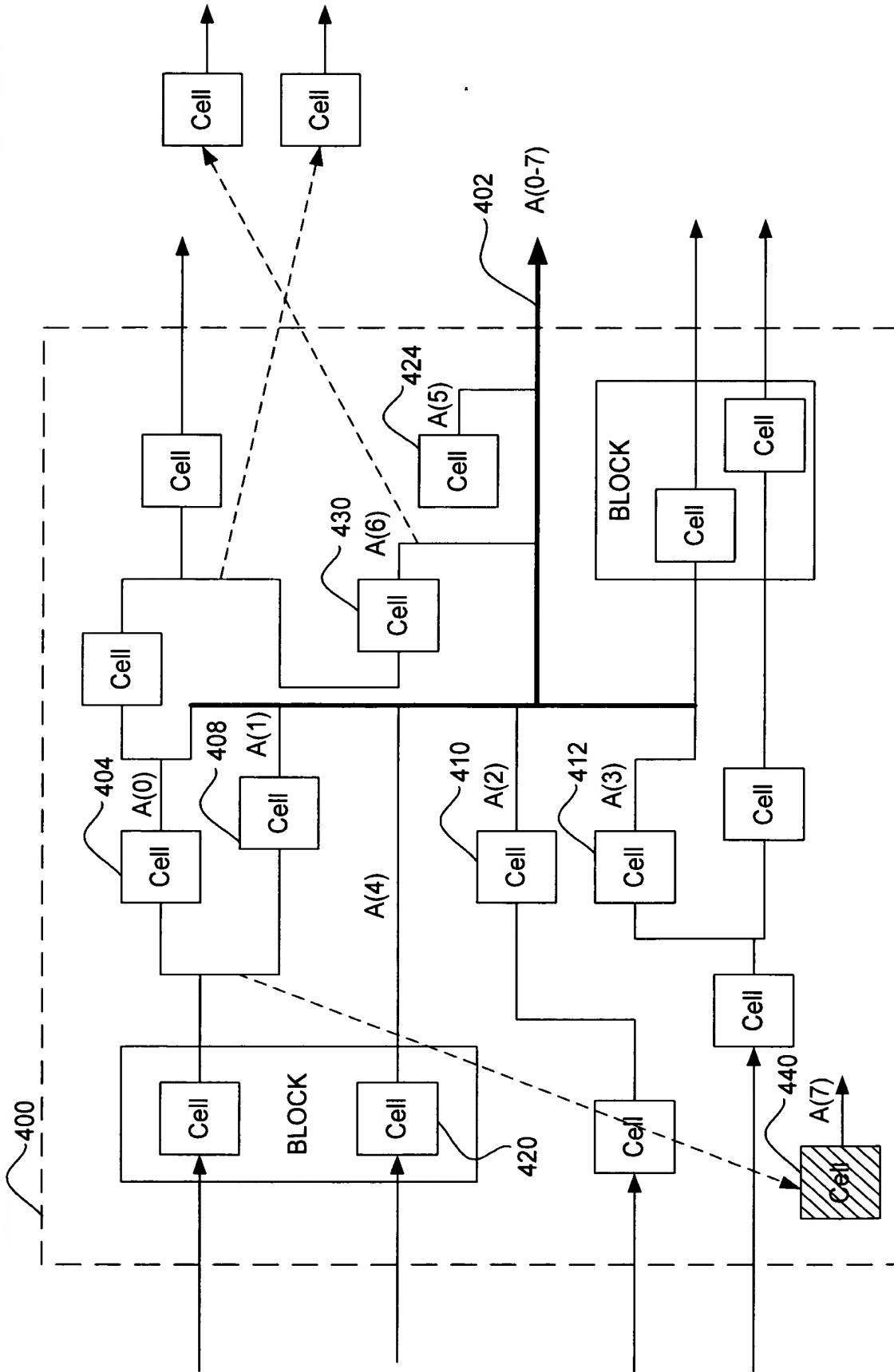


FIG. 9



SELECT PLACED SOURCES IN CONTEXT FOR NET A(0-7)

UNPLACED

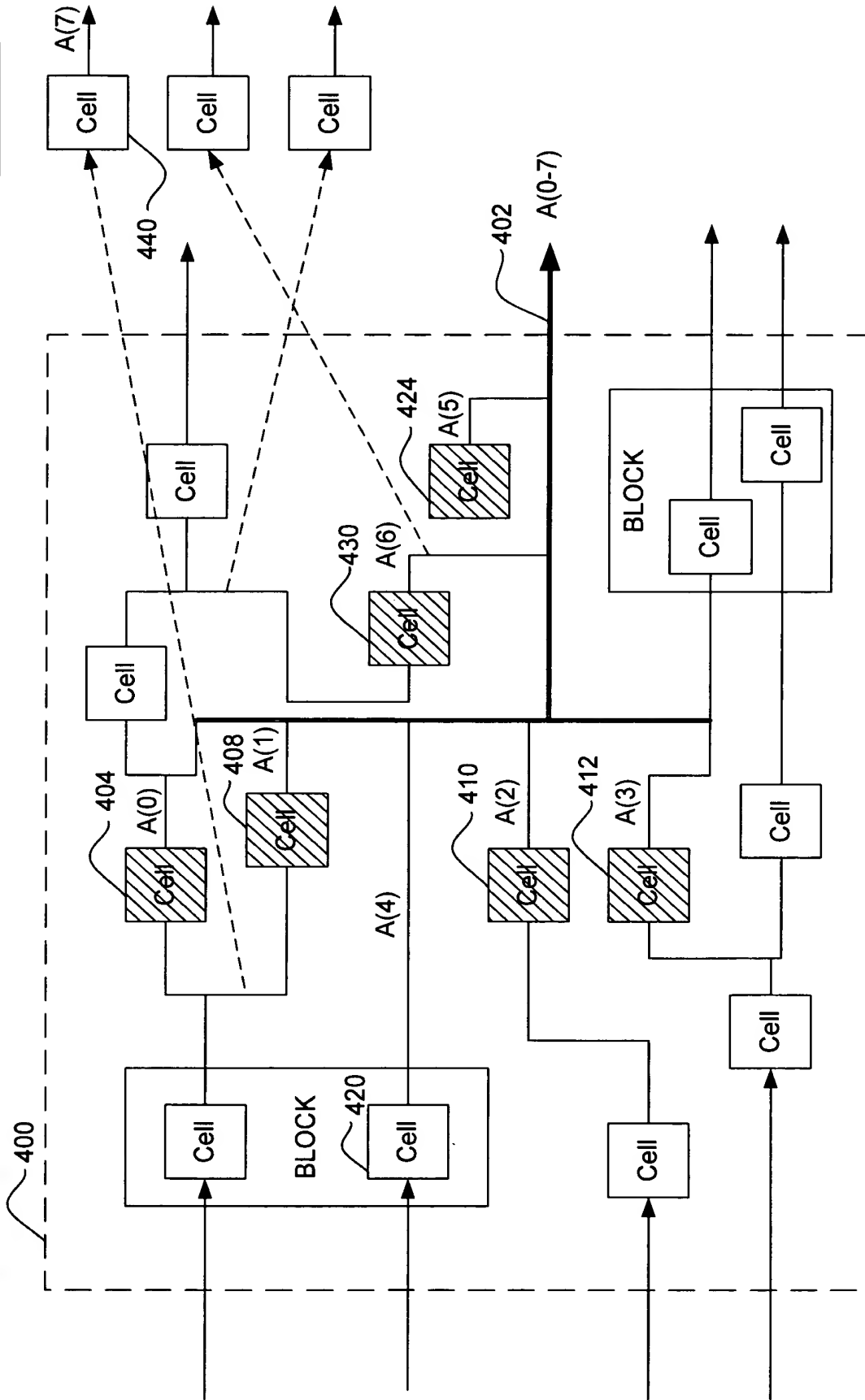


FIG. 10



UNPLACED

SELECT CELLS FOR NET A(0-7)

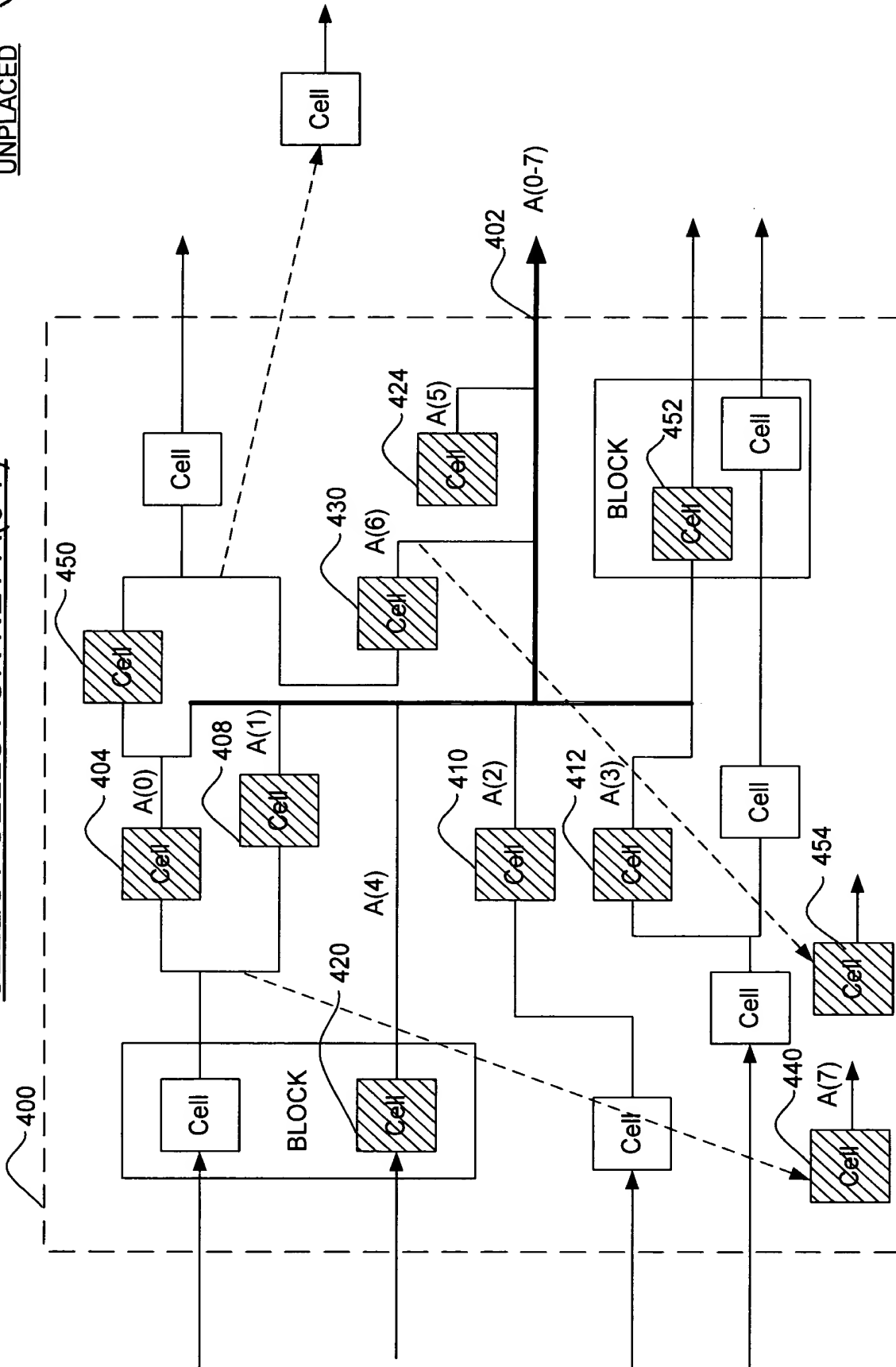
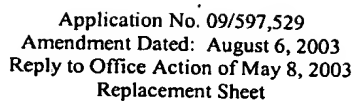


FIG. 11



UNPLACED

SELECT CELLS IN CONTEXT FOR NET A(0-7)

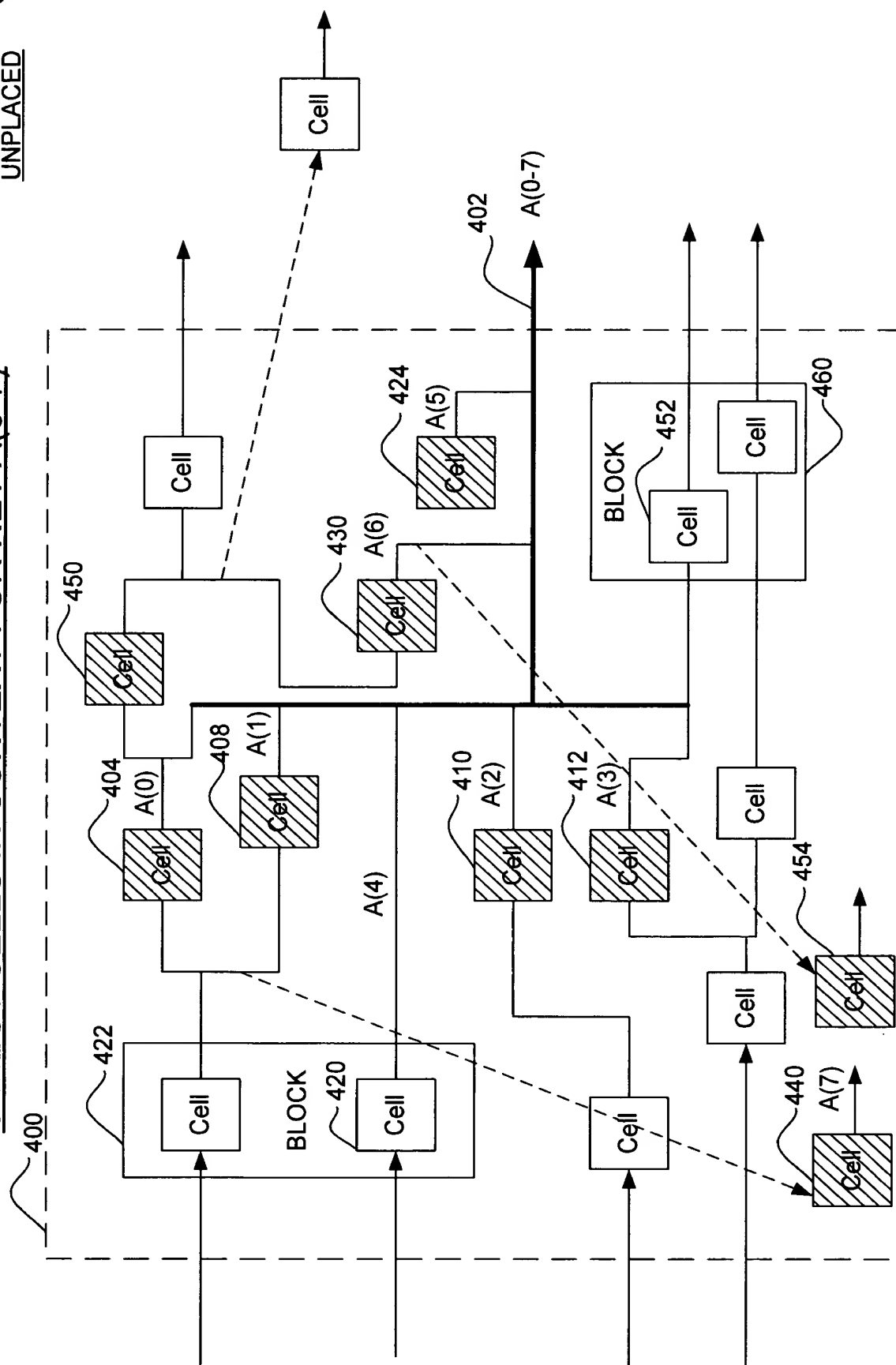


FIG 12



UNPLACED

SELECT UNPLACED CELLS IN CONTEXT FOR NET A(0-7)

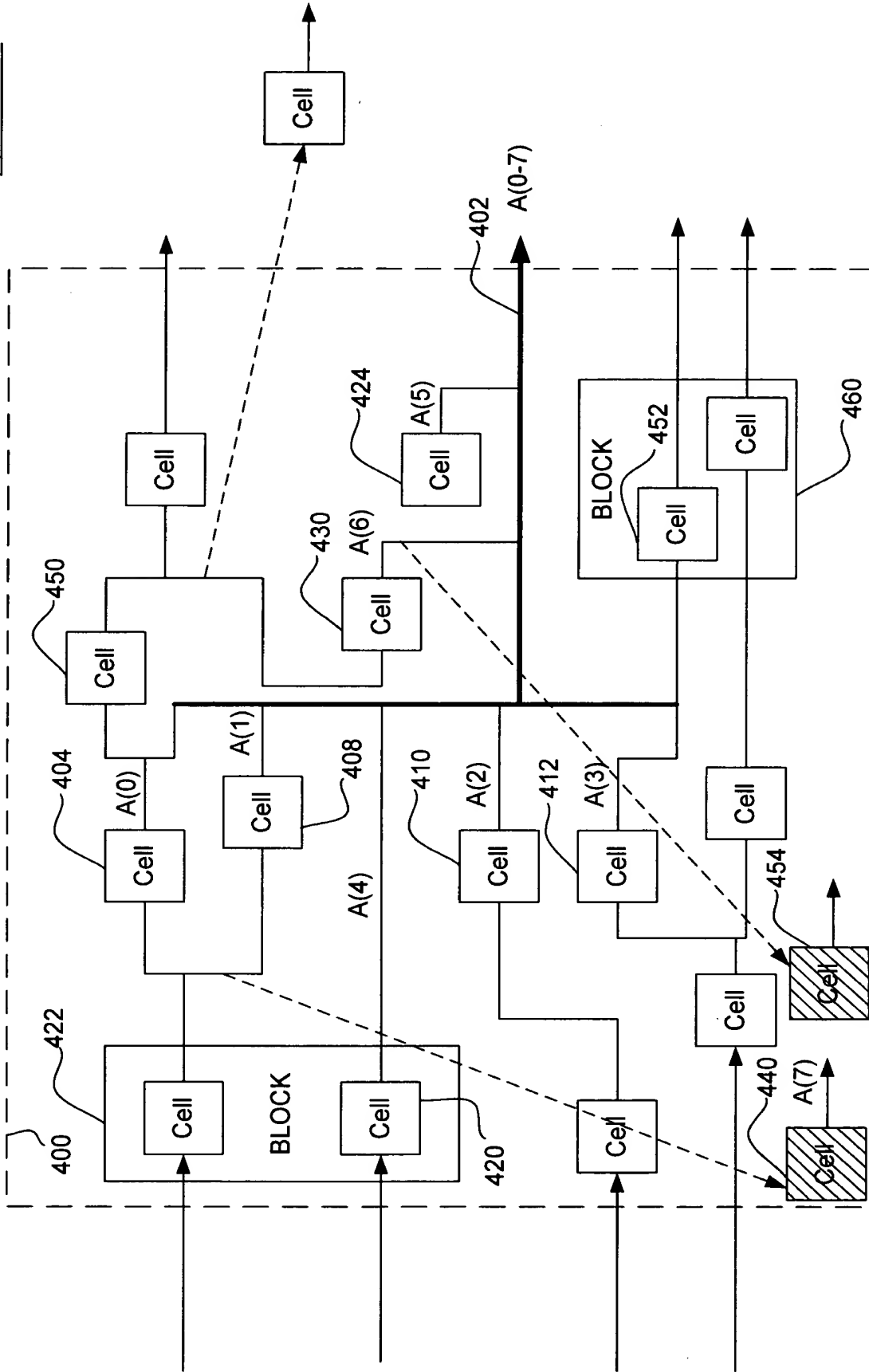
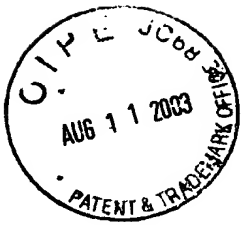


FIG. 13

[illegible]

FIG. 14



SELECT SOURCES IN CONTEXT FOR NET A(0-7) - THEN
EXECUTE ABOUT VECTOR WITH BIT ZEROS IN THE TOP ROW

UNPLACED

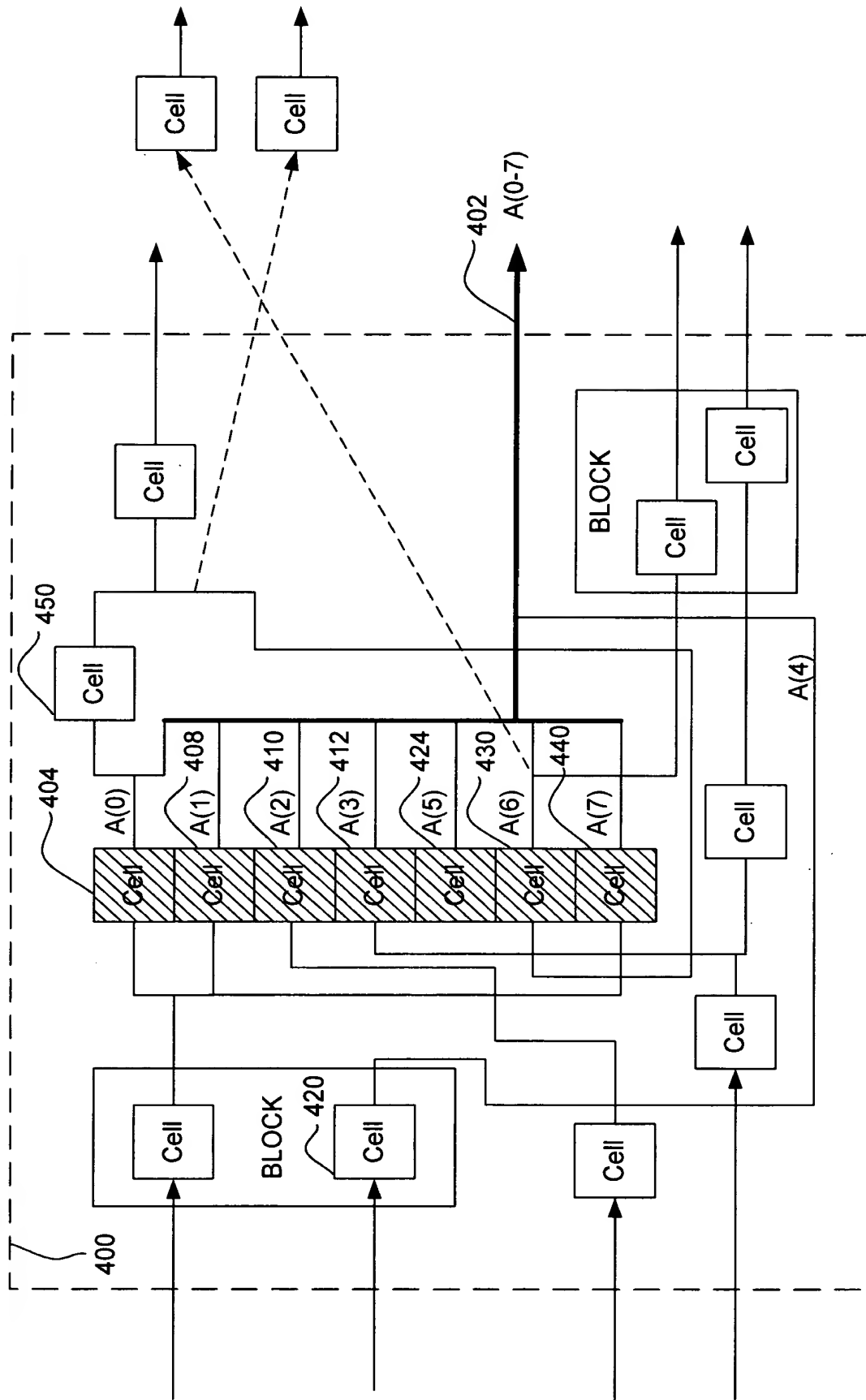
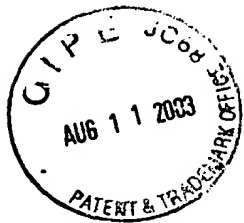


FIG. 15



SELECT CELLS FOR NET A(0-7)

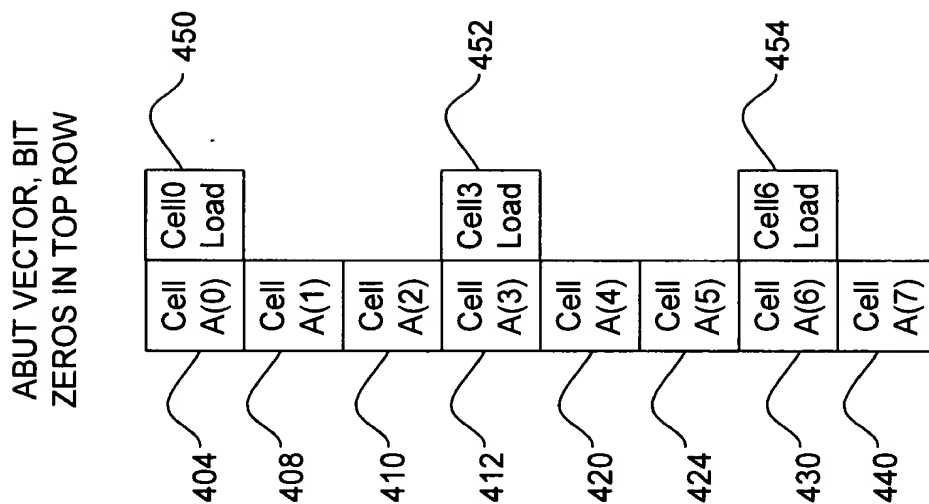


FIG. 16



SELECT CELLS FOR NET A(0-7)

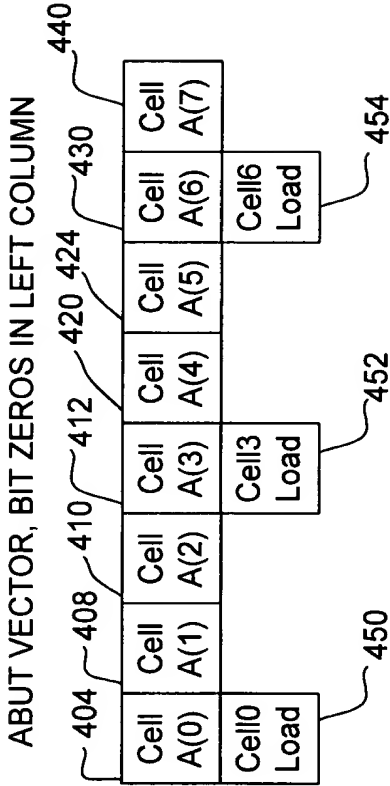


FIG. 17



SELECT SOURCES FOR NET A(0-7)

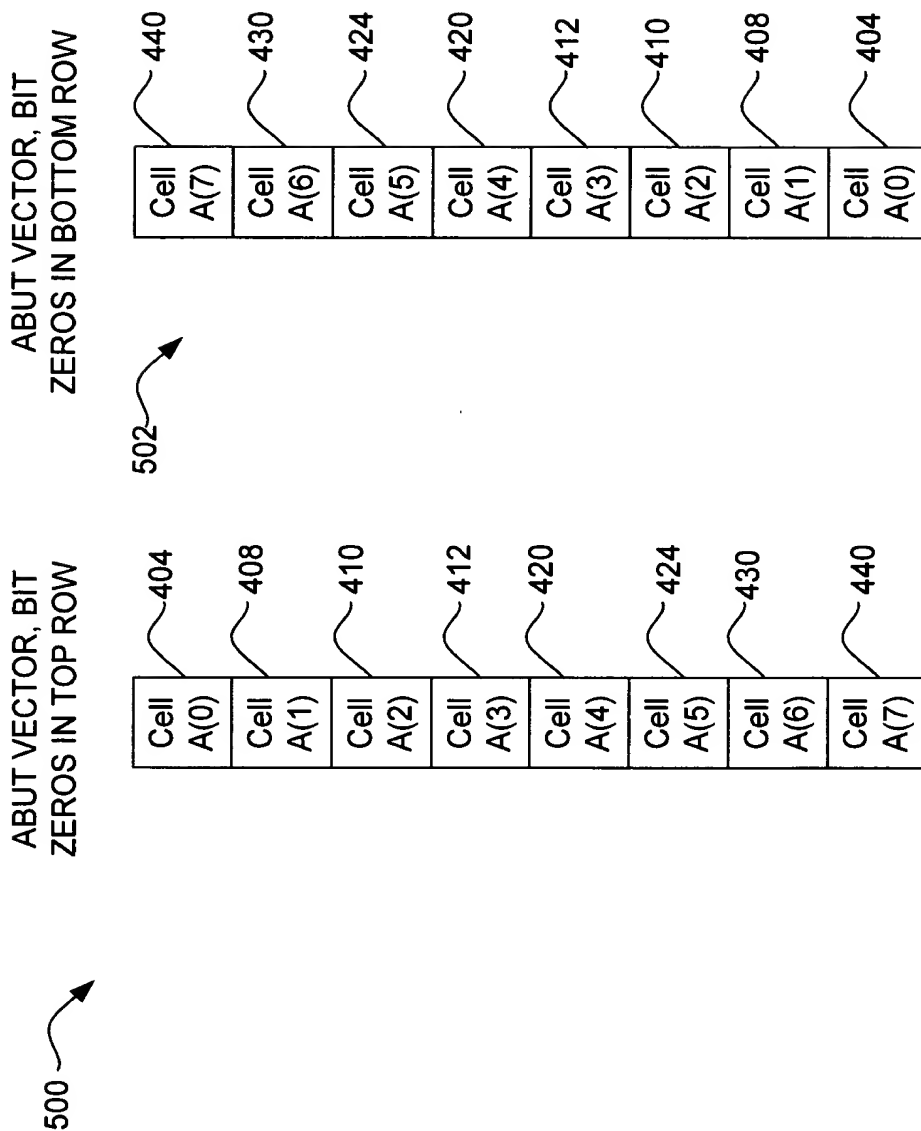
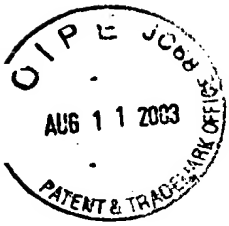


FIG. 18



SELECT SOURCES FOR NET A(0-7)

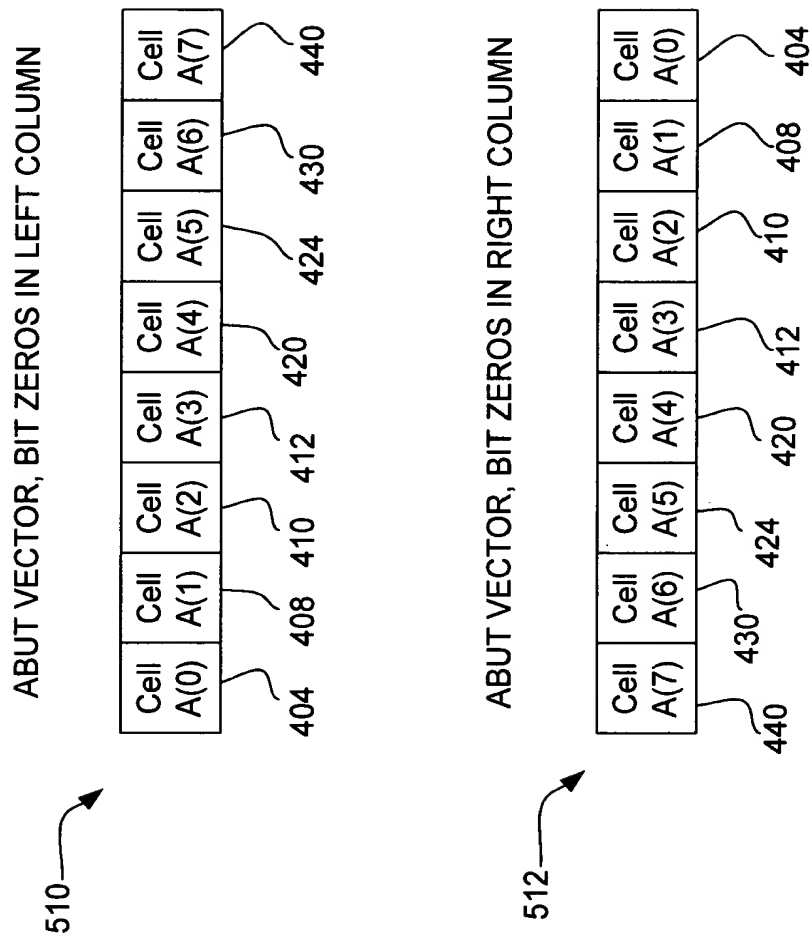
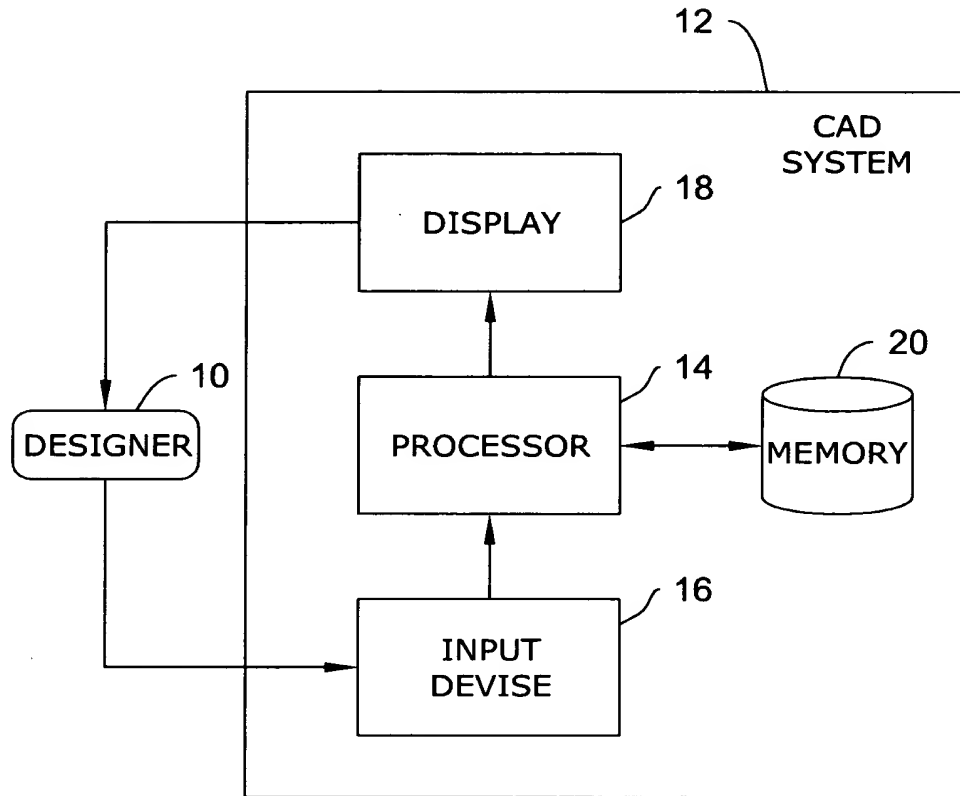


FIG. 19



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Fixed Line Quality

FIG. 1

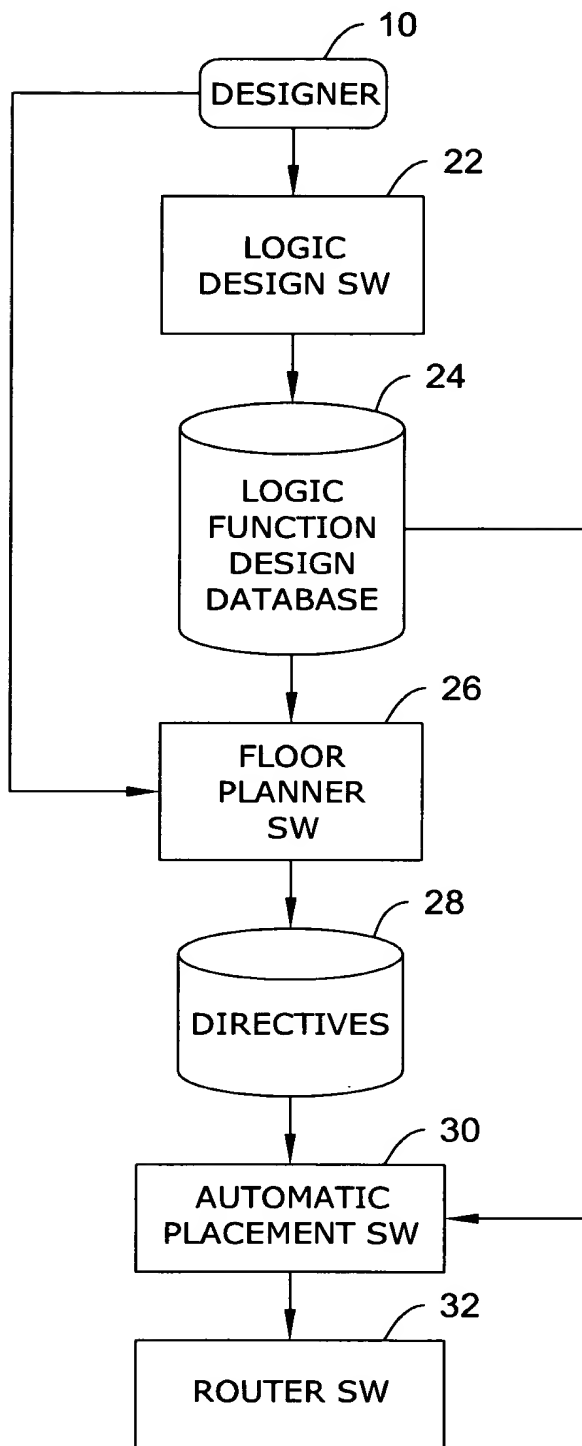


FIG.2

Fixed Line Quality

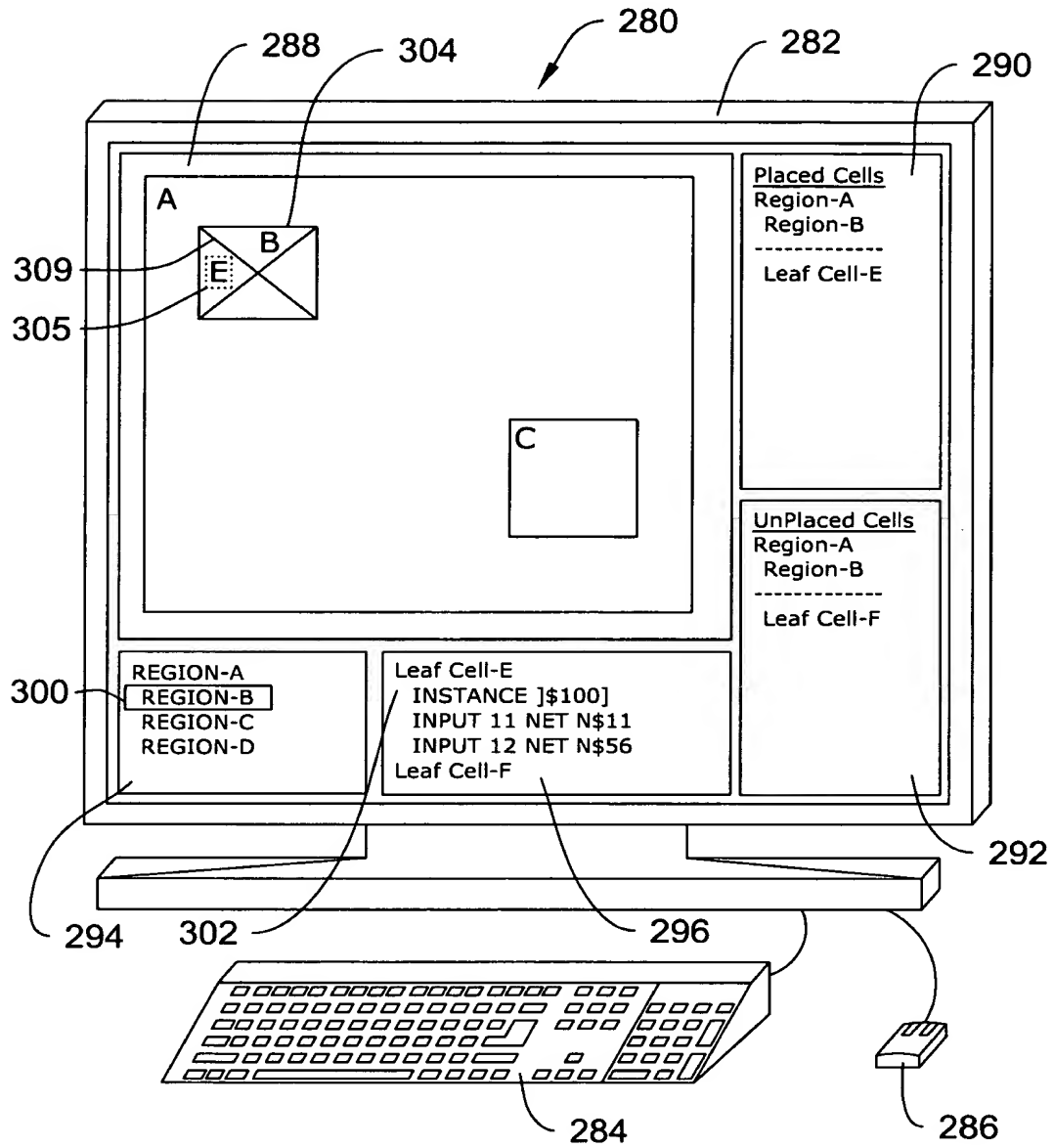


FIG. 4

Fixed Line Quality

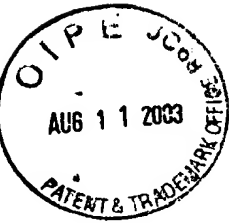
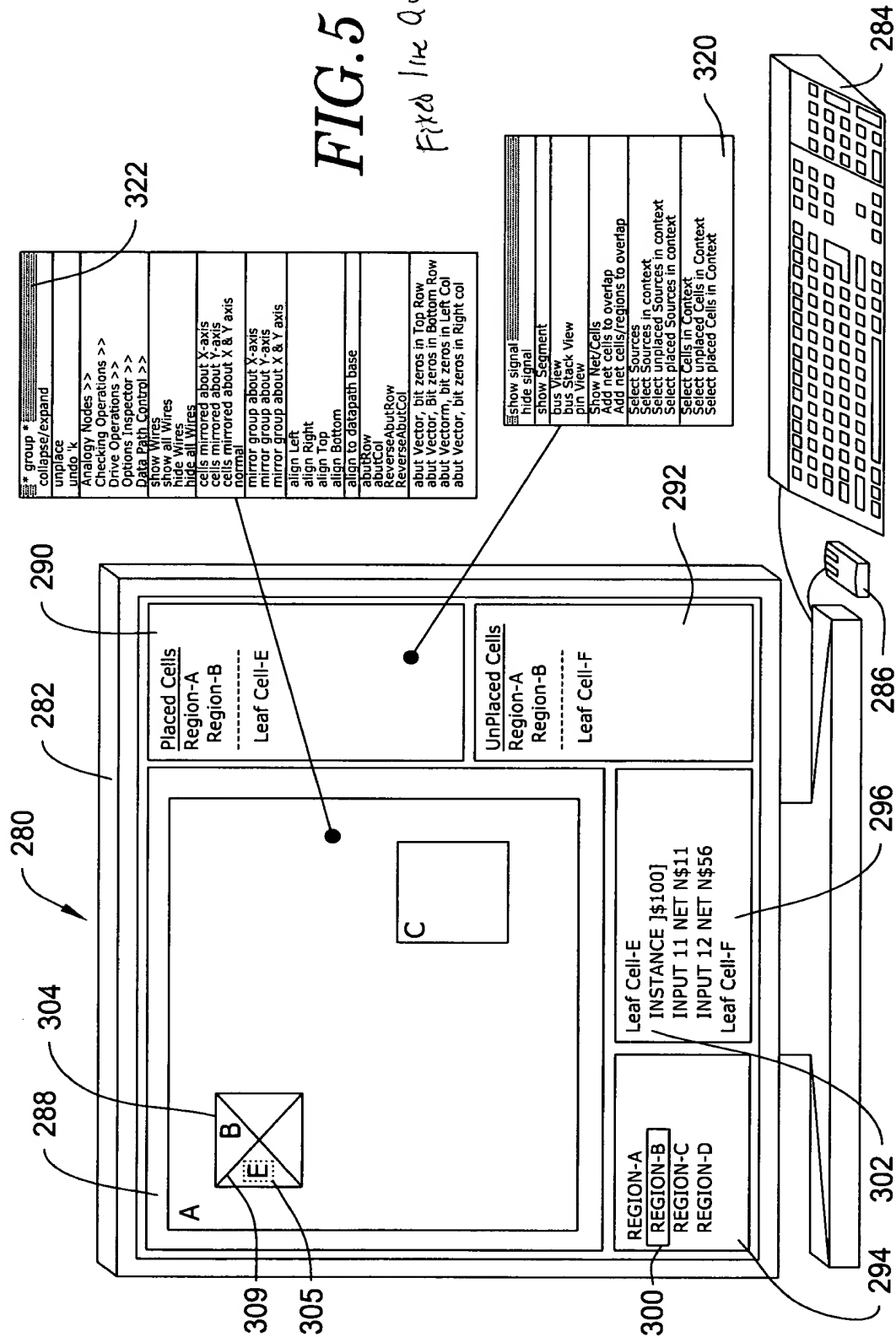


FIG. 5

Fixed 1/4 Quality







Added Arrows



SELECT SOURCES IN CONTEXT FOR NET A(0-7)

UNPLACED

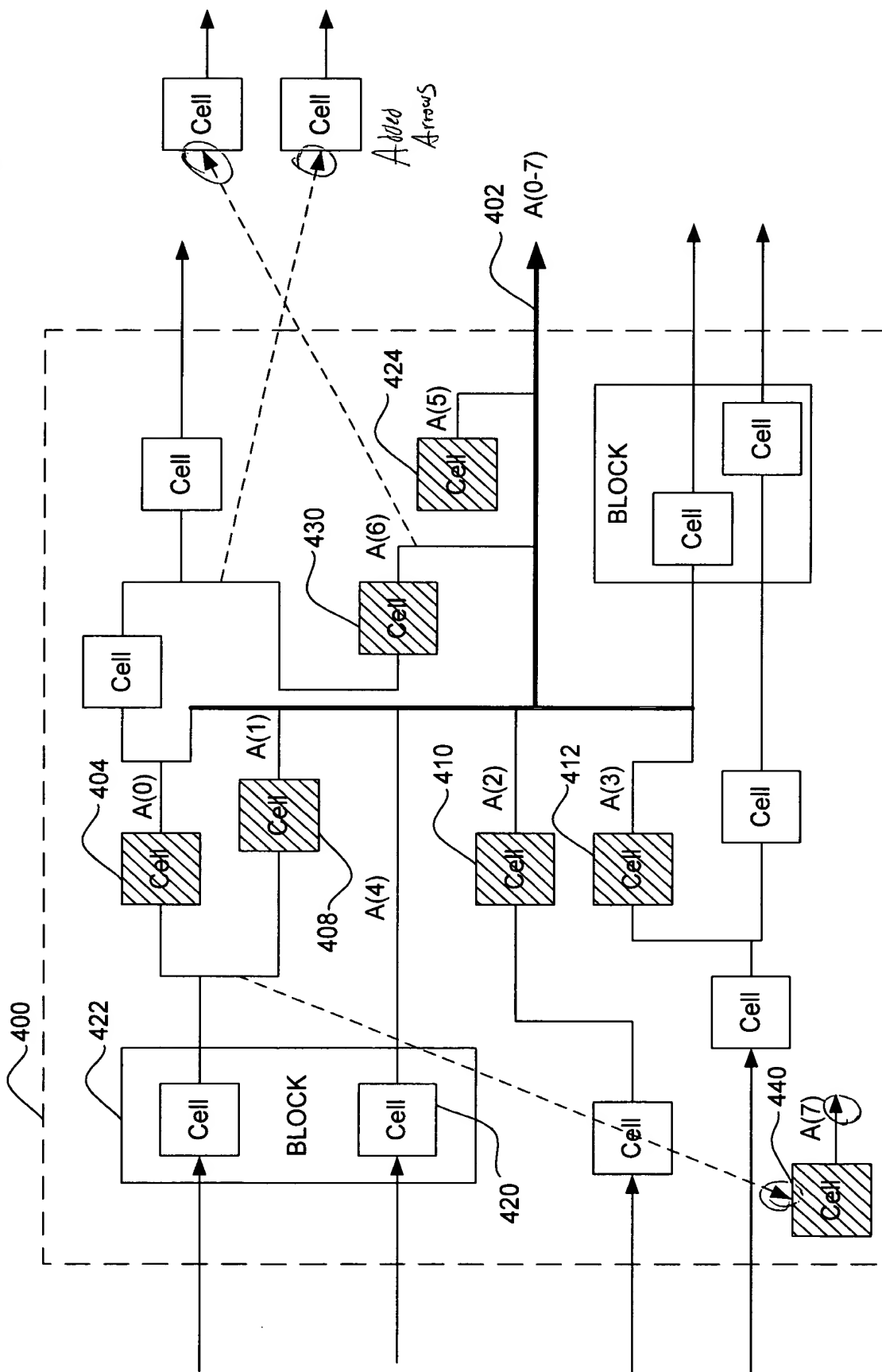


FIG. 8



SELECT UNPLACED SOURCES IN CONTEXT FOR NET A(0-7)

UNPLACED

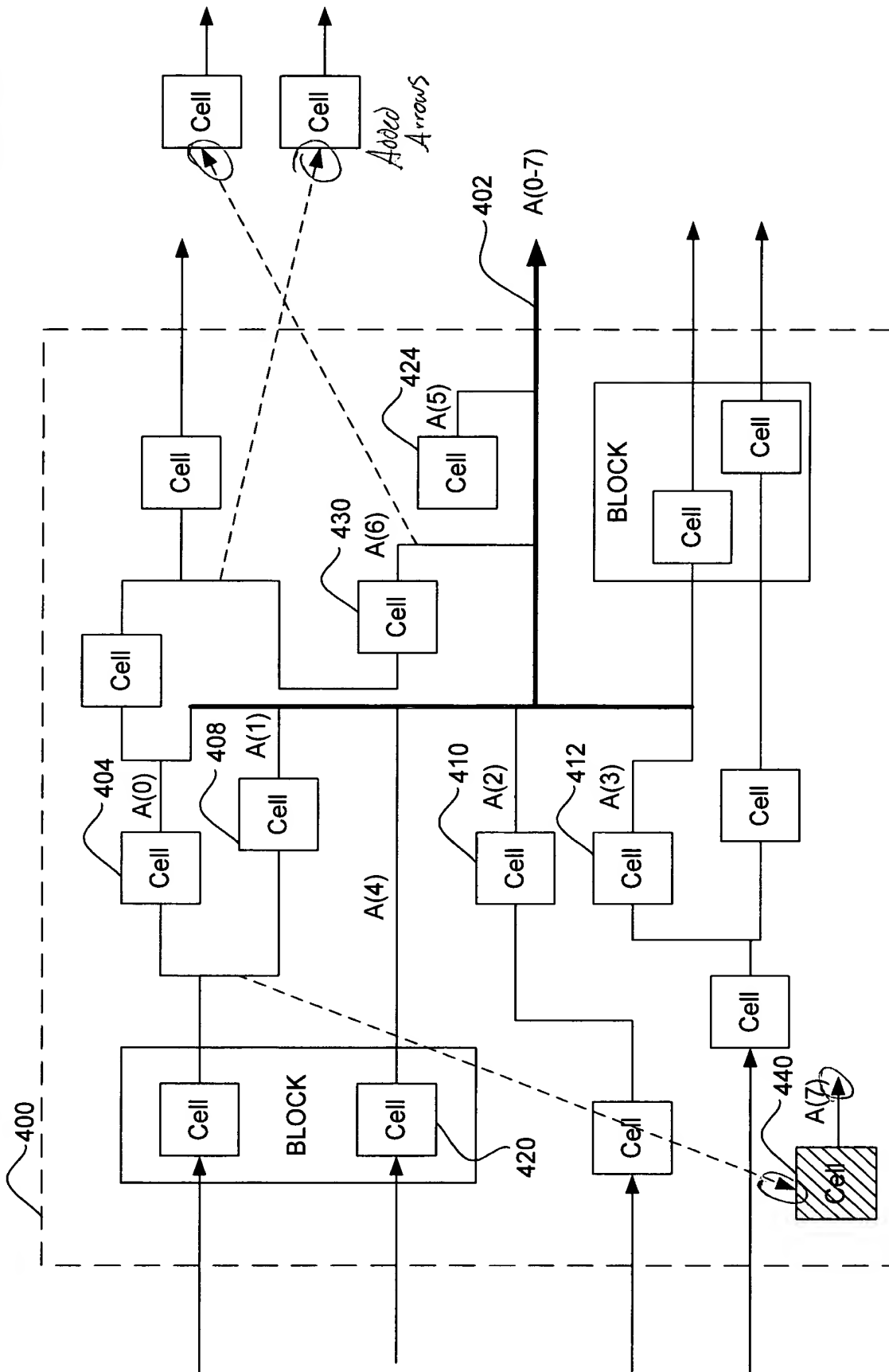


FIG. 9



SELECT PLACED SOURCES IN CONTEXT FOR NET A(0-7)

UNPLACED

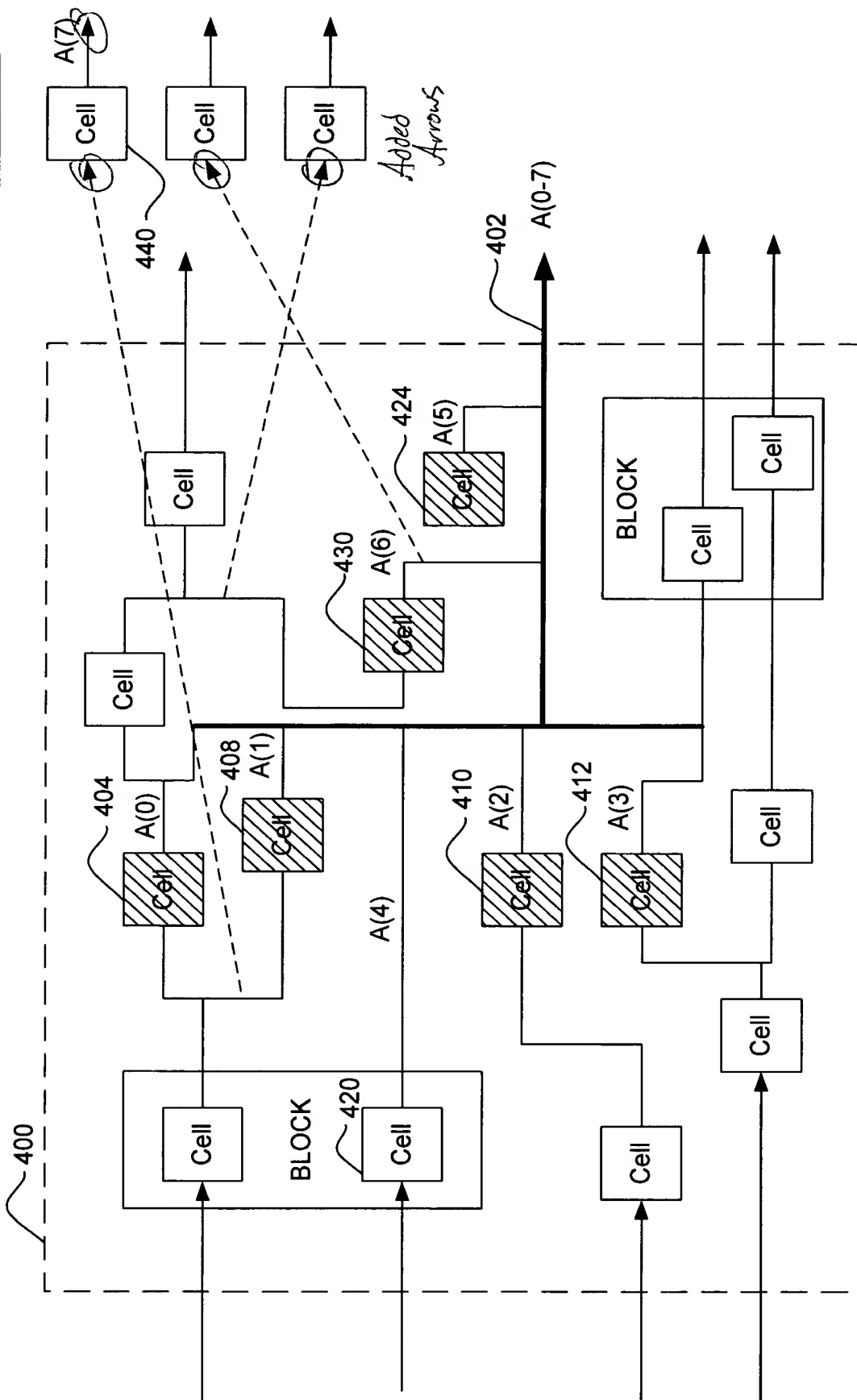


FIG. 10



UNPLACED

SELECT CELLS FOR NET A(0-7)

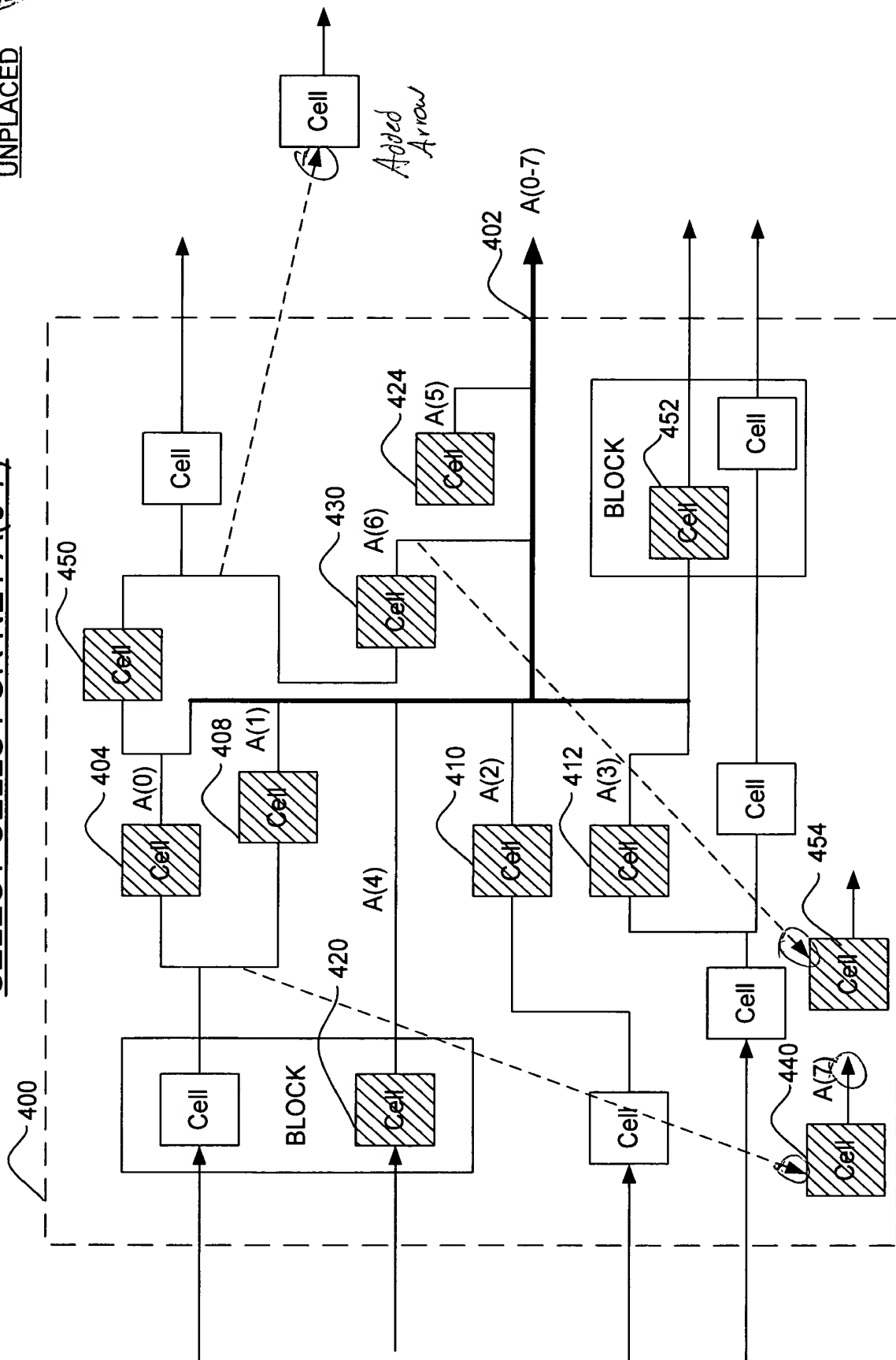


FIG. 11



SELECT CELLS IN CONTEXT FOR NET A(0-7)

UNPLACED

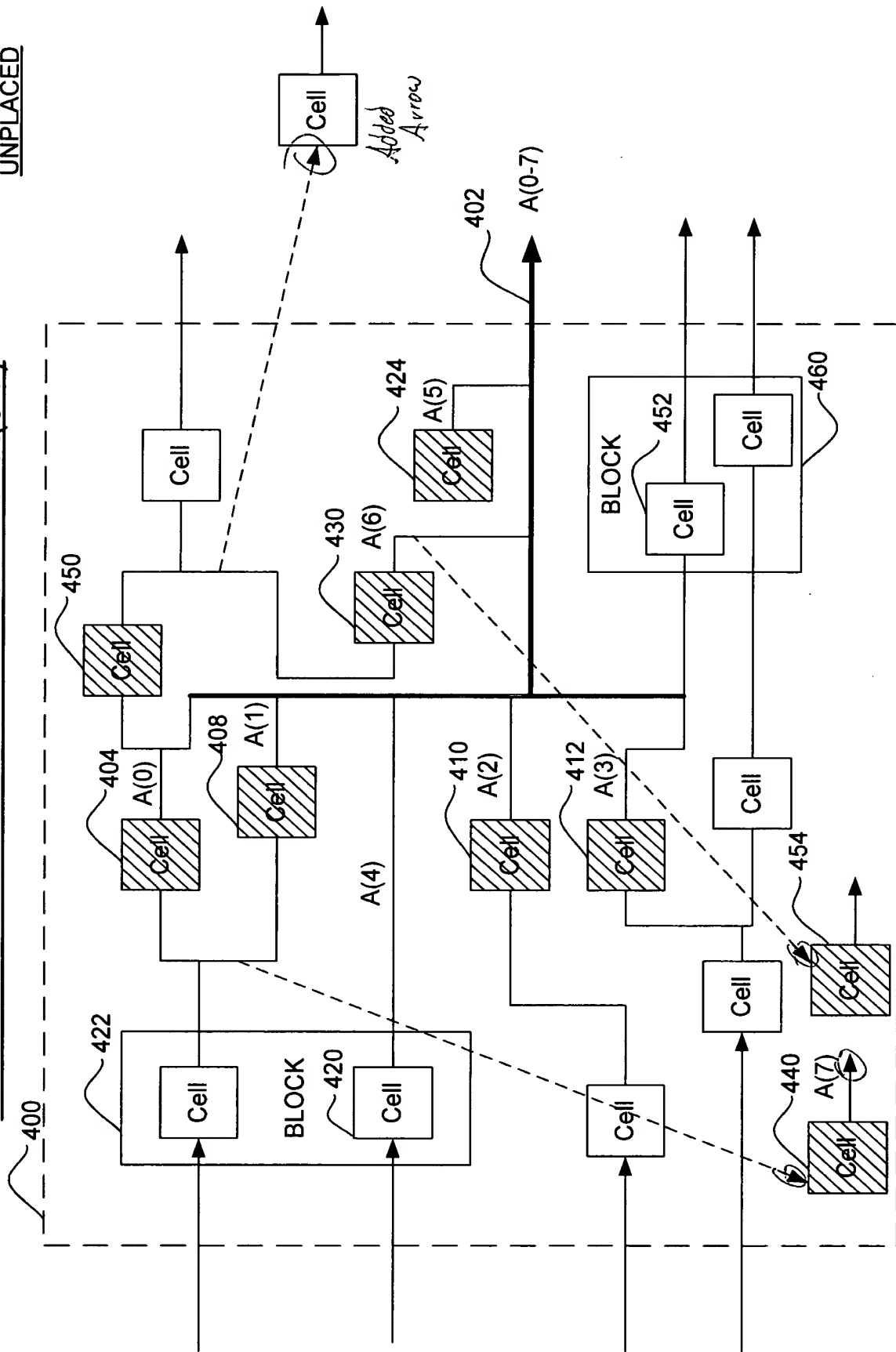


FIG 12

*Added
Arrows*

*Added
Arrow*

SELECT UNPLACED CELLS IN CONTEXT FOR NET A(0-7)

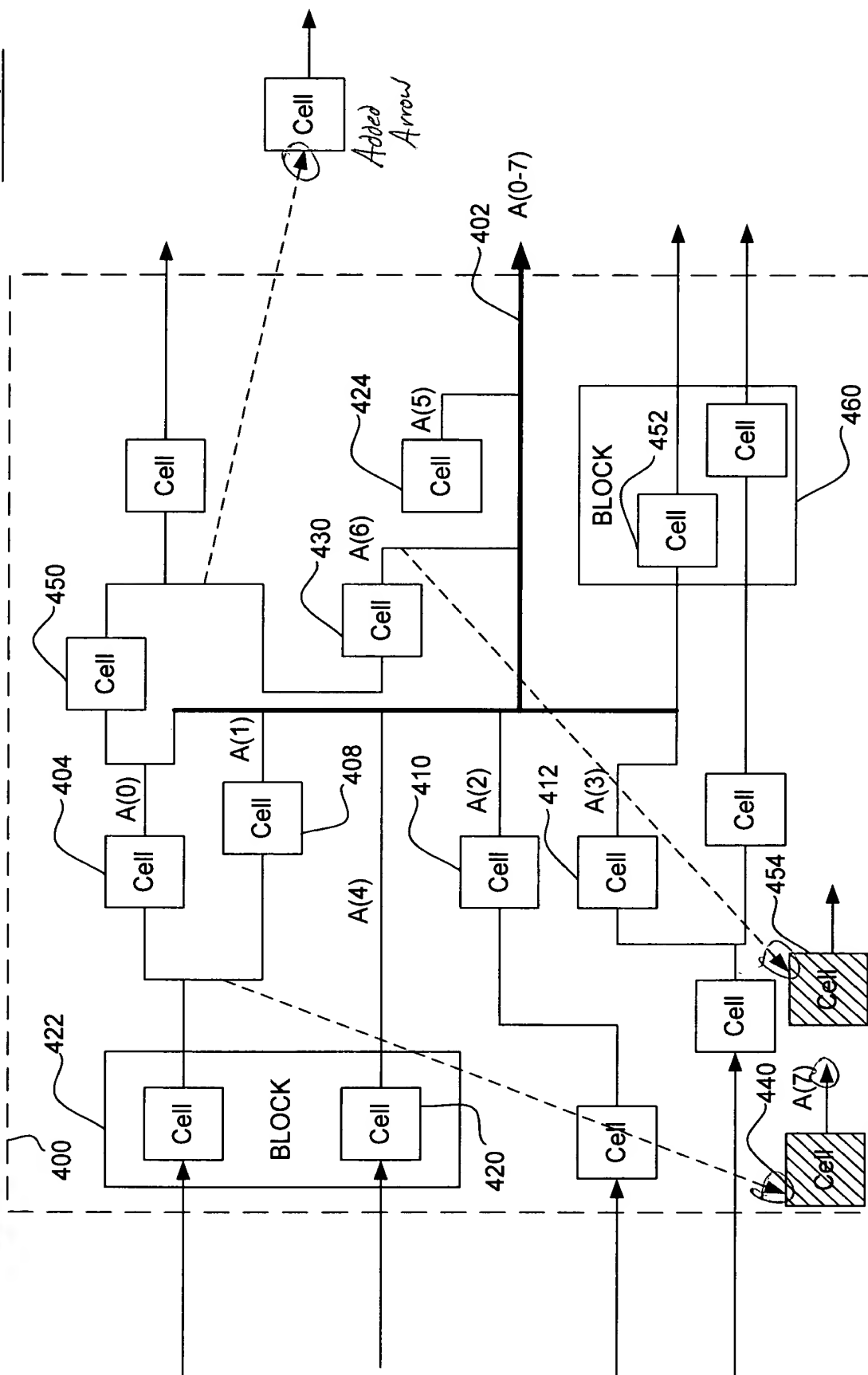
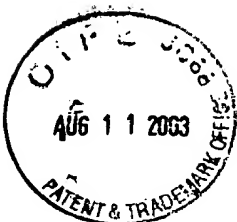


FIG. 13

Added
Arrows

Figure 1 is a block diagram of a memory access system 400. The system is divided into three sections by dashed lines. The left section contains a 'BLOCK' 422 with two 'Cell' components. The middle section contains a 'BLOCK' 420 with two 'Cell' components, and a series of 'Cell' components labeled A(0), A(1), A(4), A(2), A(3), A(5), and A(6). The right section contains a 'BLOCK' 452 with two 'Cell' components. A central bus 402 connects the sections. A handwritten note 'UNPLACED' is at the top left, and 'Address Arrows' is written vertically on the right.

FIG. 14



SELECT SOURCES IN CONTEXT FOR NET A(0-7) - THEN
EXECUTE ABOUT VECTOR WITH BIT ZEROS IN THE TOP ROW

UNPLACED

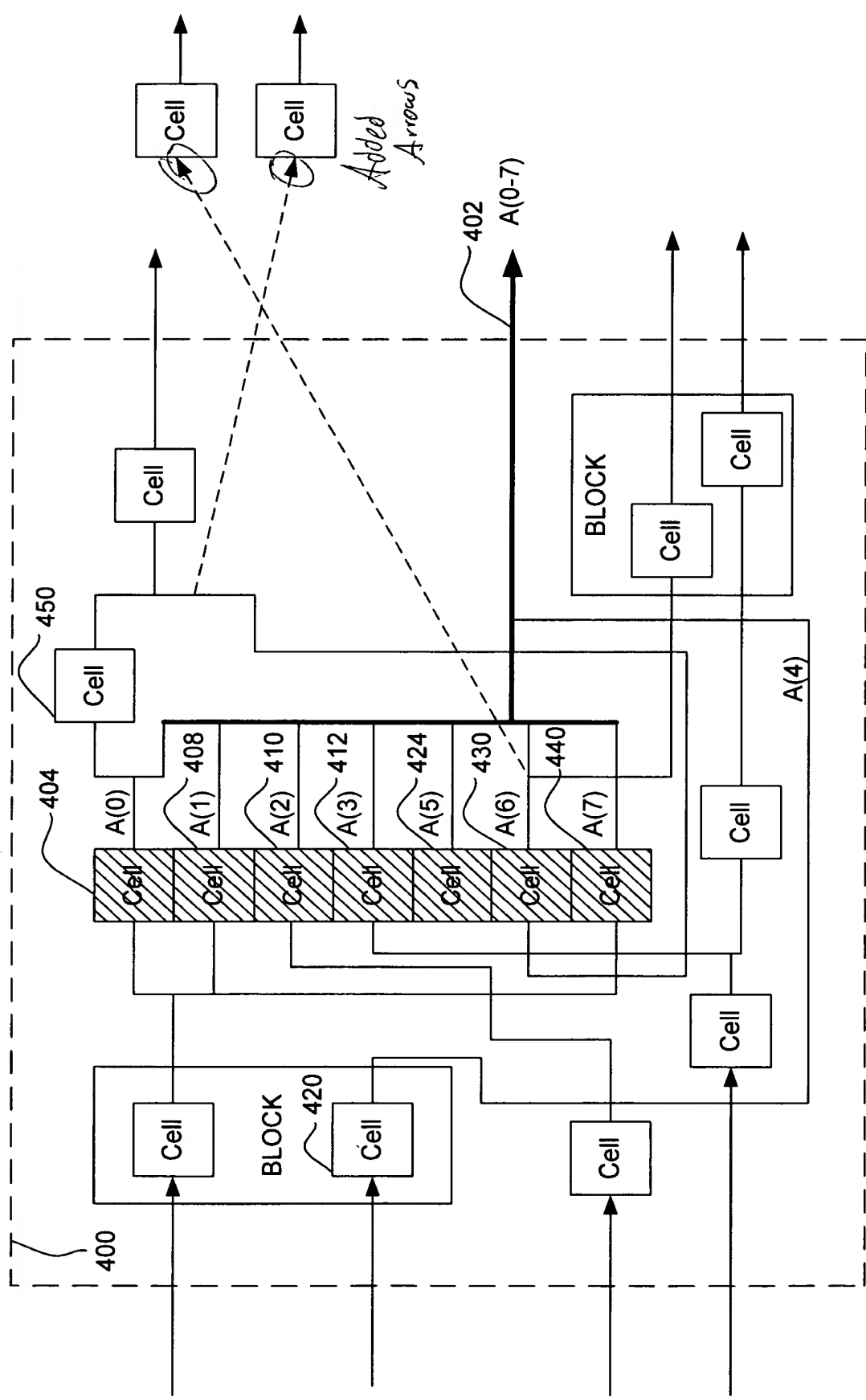


FIG. 15